Latest Trends patents rights in VLSI Design

  First Technical Talk of the **even** academic semester of the year 2016-17 under the Department of ECE was conducted on 03rd March 2017. The head of the department Prof. D V Manjunath welcomed the resource person Mr. V.D.Kulkarni, Senior Consultant, Entuple Technologies. Dean Prof Praveen was presiding over the talk. Mr V.D.Kulkarni, delivered the talk on the topic "Latest Trends patents rights in VLSI Design". The talk was elaborated on the evolution, developments and applications in the area of Electronic system design, Manufacturing and testing. He also highlighted the importance of Time and Life with real life example of missile testing in Bombay. He has also emphasized the requirement of the fundamental knowledge in any field of study. Along with this, professor briefed about the career opportunities in this emerging area of Electronic system design.

FEEDBACK ANALYSIS

RESOURCE PERSON: Mr. V.D.Kulkarni, Senior Consultant, Entuple Technologies

TOPIC: Latest Trends patents rights in VLSI Design

DATE: 3rd March, 2017

Feedback was analysed and the overall percentage was 85.1%.