# Alva's Institute of Engineering and Technology Dept. of Electronics and Communication Engineering



We cordially invite you to the

Technical Talk on

# "LATEST TRENDS PATENTS RIGHTS IN VLSI DESIGN"

by

Mr. V. D. Kulkarni Senior Consultant Entuple Technologies, Banglore

Venue: Engg. Seminar Hall Date: 3rd March 2017 by 11 AM

Mr. Ramesh B Mr. Aneesh Jain Technical Coordinator

Dr. DV Manjunatha

PROGRAMME SCHEDULE

Invocation

Welcome Speech

Introduction of Chief Guest

Technical talk

Honoring the chief guest

Vote of thanks

Staff & Students
Department of E & C Engineering



### Alva's Institute of Engineering & Technology Shobhavana Campus, Mijar, Moodbidri, D.K - 574225 Phone: 08258-262725, Fax: 08258-262726

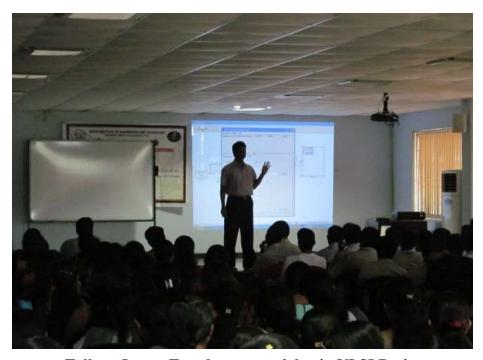
### DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

# Latest Trends patents rights in VLSI Design

First Technical Talk of the **even** academic semester of the year 2016-17 under the Department of ECE was conducted on 03<sup>rd</sup> March 2017. The head of the department Prof. D V Manjunath welcomed the resource person Mr. V.D.Kulkarni, Senior Consultant, Entuple Technologies. Dean Prof Praveen was presiding over the talk.



Welcoming of Resource person



Talk on Latest Trends patents rights in VLSI Design



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# DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Mr V.D.Kulkarni, delivered the talk on the topic "Latest Trends patents rights in VLSI Design". The talk was elaborated on the evolution, developments and applications in the area of Electronic system design, Manufacturing and testing. He also highlighted the importance of Time and Life with real life example of missile testing in Bombay. He has also emphasized the requirement of the fundamental knowledge in any field of study. Along with this, professor briefed about the career opportunities in this emerging area of Electronic system design.



Token of Gratitude for the resource person

Outcome: Students gained knowledge of patent rights in a VLSI Domain and its various technologies which encourages the students to take up project and higher studied in the VLSI Domain.

Head of the Department

H. O. D.

D.V. Tu

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